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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Ap	plication of:	) Examiner: Gene Auduong
1	Kenneth S. McElvain, et al.	)
Application No.: 09/313,225		) Art Unit: 2818
Filing D	Pate: May 17, 1999	
F	METHODS AND APPARATUSES FOR DESIGNING INTEGRATED CIRCUITS	) Confirmation No. 3604 ) Date of NOA: September 10, 2002
	ommissioner for Patents gton, D.C. 20231	
	SUBMISSION O	F FORMAL DRAWINGS
Dear Sir	•	
(24) she	ets and thirty (30) figures for the Res	te set of formal drawings, comprising twenty-four above-identified application.  Spectfully submitted,  AKELY, SOKOLOFF, TAYLOR & ZAFMAN  Les C. Scheller, Jr.  2. No. 31,195
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